EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	15497	((438/133,106,15,128,121) or (257/678) or (716/16,3,17,18) or (326/37,38,39)).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 10:27
S2	3	S1 and (field with program$3 with platform with array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:30
S5	2	("20050166170").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 10:31
S3	1283	S1 and (platfrom with logic with device "PLD")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:31
S4	188	S1 and (platfrom with logic with device "PLD") and (dic$3 cut$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:33
S6	11	S1 and (platfrom with logic with device "PLD") and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:37
S8	0	S6 not S7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:38
S11	801	S10 and (silicon "Si")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:39

S10	2319	S1 and (platfrom with logic with (unit device) "PLD" fiel near3 program$3 near7 array "FPGA" "FPPA")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:39
S12	33	S11 and (cut$3 dic$3) with (wafer substrate array unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:40
S13	208	(platfrom with logic with device "PLD" fiel near3 program$3 near7 array) and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:53
S9	11	S1 and (platfrom with logic with (unit device) "PLD" fiel near3 program$3 near7 array) and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:53
S7	11	S1 and (platfrom with logic with device "PLD" fiel near3 program$3 near7 array) and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:53
S15	463	(platfrom with logic with (unit arry device) "PLD" field near3 program$3 near7 array "FPGA" "FPAA") and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:55
S14	12	S1 and (platfrom with logic with (unit device) "PLD" field near3 program$3 near7 array) and (dic$3 cut$3) with (wafer substrate)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 10:55
S17	78	("NRE" Non$1recurring near3 engineering) and S1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 12:40

S18	3	("NRE" Non$1recurring near3 engineering) and (Cut$3 dic$3) with (substrate wafer) and (platform near3 logic "PLD")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 12:42
S19	211	("NRE" Non$1recurring near3 engineering) and (ASIC application near3 integrated near3 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 12:43
S20	15	("NRE" Non$1recurring near3 engineering) and (ASIC application near3 integrated near3 circuit) and (cut$3 dic$3) with (wafer substrate platform arry unit board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 12:44
S21	2	("6953956").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/29 12:47
S16	334	S15 and (silicon "Si")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 13:38
S22	197	S16 and program$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 13:39
S23	9	(US-20020072135-$ or US-20040161878-$ or US-20050062495-$ or US-20040061147-$).did. or (US-6910201-$ or US-6953956-$ or US-6362651-$ or US-6157213-$ or US-6222212-$).did.	US-PGPUB; USPAT	OR	ON	2006/11/29 16:07
S24	9	S23 and (program$3) and (silicon "Si")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/29 16:09

S25	8021	program$3 near5 (platform wafer substrate chip) and (silicon "Si")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:43
S27	1026	S25 and S26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:45
S26	266113	(dic$3 cut$3) with (wafer substrate chip platform aray unit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:45
S30	11	S29 not S28	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:47
S29	19	S27 and (program$3 with (firmware))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:47
S28	8	S27 and (program$3 with (firmware)) and (storage with network)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 12:47
S33	2	S31 and (digital with signal "DSP")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:33
S32	0	S31 and (network with storage "SAN") and (digital with signal "DSP")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:33

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S36	608	S25 and S35	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:35
S37	17	S36 and (firmware) and (Field with program$3 with array FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:42
S35	45565	(DSP digital with signal) and (Storage near3 area near3 network SAN)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:44
S41	23	S40 and (dic$3 cut$3) and (field with program$3 FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:46
S40	1060	(DSP digital with signal) and (Storage near3 area near3 network)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 14:54
S42	217	S40 and (field with program$3 FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 15:30
S43	32	(Silicon "Si") and (storage with area with network) and (digital near3 process)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 16:56
S44	2	("20040098538").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/04 17:41

S45	2	S44 and (FPGA and storage adj area adj network)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/04 17:43
S47	2	("6507923").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/05 10:11
S46	9	(FPGA program$3 adj Gate adj array) with storage adj area adj network	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 10:11
S49	10	(US-20020072135-$ or US-20050062495-$ or US-20040161878-$ or US-20040061147-$).did. or (US-6910201-$ or US-6953956-$ or US-6362651-$ or US-6222212-$ or US-6157213-$ or US-5822256-$).did.	US-PGPUB; USPAT	OR	ON	2006/12/05 10:12
S31	10	(US-20020072135-$ or US-20050062495-$ or US-20040161878-$ or US-20040061147-$).did. or (US-6910201-$ or US-6953956-$ or US-6362651-$ or US-6222212-$ or US-6157213-$ or US-5822256-$).did.	US-PGPUB; USPAT	OR	ON	2006/12/05 10:12
S50	1	S49 and (DSP digital near3 process)	US-PGPUB; USPAT	OR	ON	2006/12/05 11:01
L1	2	("6222212").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/05 15:25
L2	8	field adj program$3 with (core and processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 15:44

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L3	69	field adj program$3 with (software firmware)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 16:10
L5	7329	flip adj chip and encapsulat$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 16:17
L4	63	field adj program$3 with (software)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 16:17
L6	590	5 and silicon and program$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 16:43
L7	368	5 and silicon and program$3 and (copper "Cu")	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/05 17:44
L8	10	(("6370677") or ("5581738") or ("5581742") or ("6397169") or ("20030099254")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/05 17:46